

<b>Notice of References Cited</b>	Application/Control No. 09/977,207	Applicant(s)/Patent Under Reexamination MIURA ET AL.	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,668,403	09-1997	Kunikiyo, Tatsuya	257/639
*	B	US-5,413,950	05-1995	Chen et al.	438/253
*	C	US-5,376,571	12-1994	Bryant et al.	438/257
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	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, 1986 Lattice Press, pages 198, 305, 322 and 323.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.